

## AMENDMENTS TO THE CLAIMS

Claim listing:

1-11. (Cancelled)

12. (New) In a memory component including a dummy cell with a dummy bit line and a plurality of wordlines, the memory component having a memory component capacitance and memory component operational characteristics, including dummy bitline capacitance of the dummy bitline, a method of implementing a diffusion replica delay circuit comprising:

- a. coupling a diffusion replica capacitor to the dummy bit line and one wordline of the plurality of wordlines, coupling the diffusion replica capacitor to the memory component, storing in the diffusion replica capacitor a predetermined replica charge representative of the dummy bitline capacitance, and matching the diffusion replica capacitance of the diffusion replica capacitor to the dummy bitline capacitance, and
- b. coupling a diffusion replica transistor with the diffusion replica capacitor, and coupling the diffusion replica transistor between the diffusion replica capacitor and a charge sink, the transistor being disposed to control the magnitude of the predetermined replica charge.

13. (New) The method of Claim 12, wherein the diffusion replica capacitance is substantially a predetermined fraction of the dummy bitline capacitance.

14. The method of Claim 12, and further comprising coupling the dummy cell with a memory cell having local bitlines and local wordlines, and providing a limited voltage swing signal to at least one of the local bitlines and the local wordlines.

15. (New) The method of Claim 12, comprising operably coupling the dummy cell and additional dummy cells with a selected wordline decoder and a selected sense amplifier.

16. (New) The method of Claim 15, and further comprising coupling the dummy cells with memory cells, each having local bitlines and local wordlines, and providing a limited voltage swing signal to at least one of the local bitlines and the local wordlines.

17. (New) The method of Claim 16, wherein the dummy cells comprise the dummy bitline and additional dummy bit lines.